

**Amendments to the Specification**

Please delete the paragraph beginning on page 8, lines 10 through 11.

Please replace the paragraph beginning on page 8, line 13, with the following amended paragraph:

Figure ~~1B~~ **1A** is a flow diagram illustrating the various components of one embodiment of the present invention implemented on a single semiconductor platform;

Please replace the paragraph beginning on page 15, line 4, with the following amended paragraph:

~~Figures 1 and 1A show~~ Figure 1 shows the prior art. Figures ~~1B 1A-32C~~ show a graphics pipeline system of the present invention.

Please replace the paragraph beginning on page 15, line 7, with the following amended paragraph:

Figure ~~1B~~ **1A** is a flow diagram illustrating the various components of one embodiment of the present invention. As shown, the present invention is divided into four main modules including a vertex attribute buffer (VAB) **50**, a transform module **52**, a lighting module **54**, and a rasterization module **56** with a set-up module **57**. In one embodiment, each of the foregoing modules is situated on a single semiconductor platform in a manner that will be described hereinafter in greater detail. In the present description, the single semiconductor platform may refer to a sole unitary semiconductor-based integrated circuit or chip.